

Firmware Synthesis for Ultra-Thin IoT Devices Based on Model Integration

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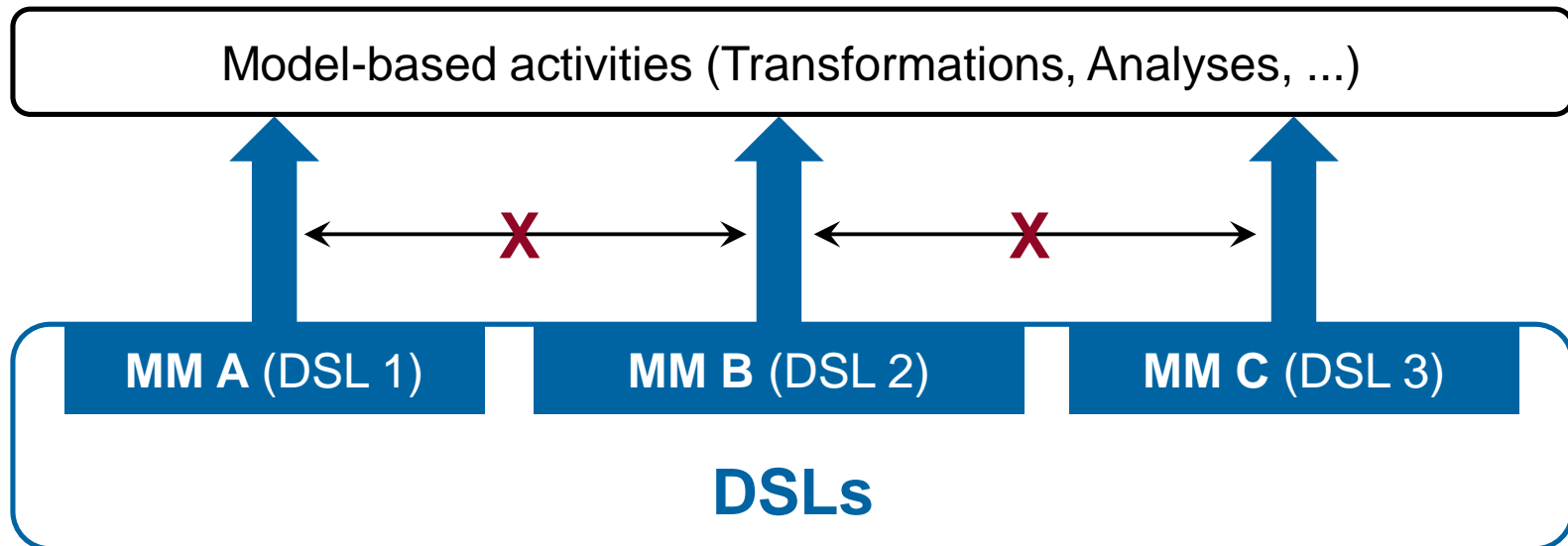
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**International Workshop on Modeling Language Engineering
and Execution 2019**

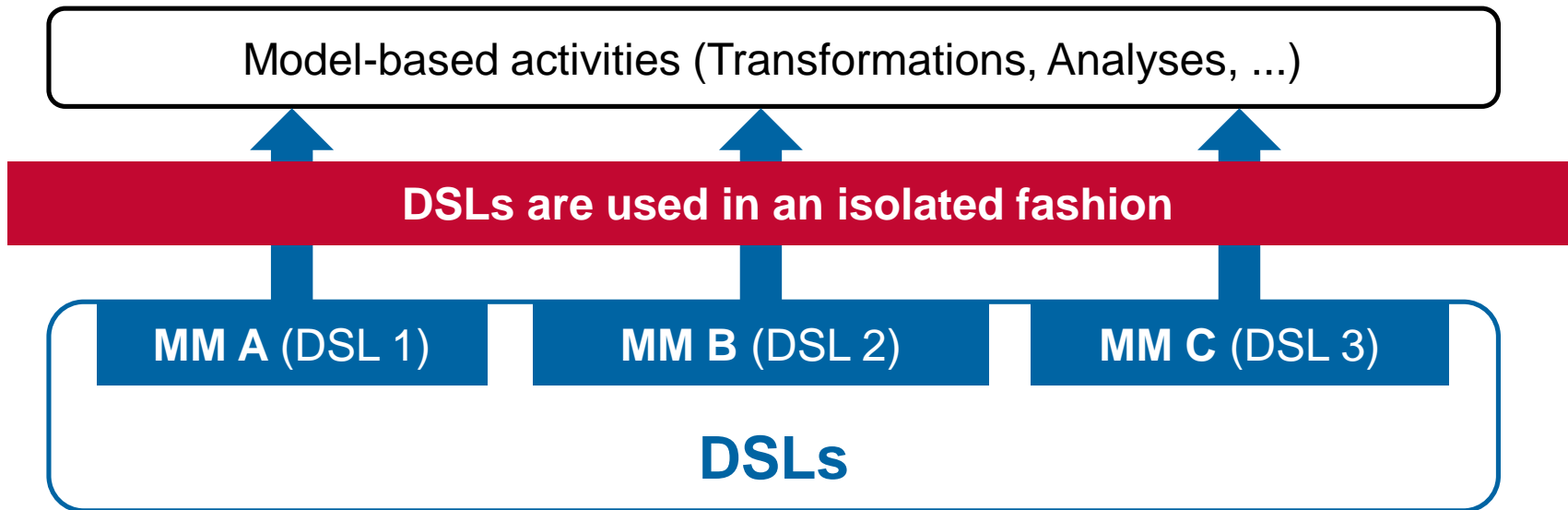
Introduction & Motivation

- FW for ultra-thin IoT devices challenging to develop
 - Resource constraints (Power, memory...)
 - Extensive FW functionalities (RT computing, security, safety, ...)
 - Market pressure (Short time-to-market)
- MD approaches can tackle some of these issues, variety of different DSLs are used



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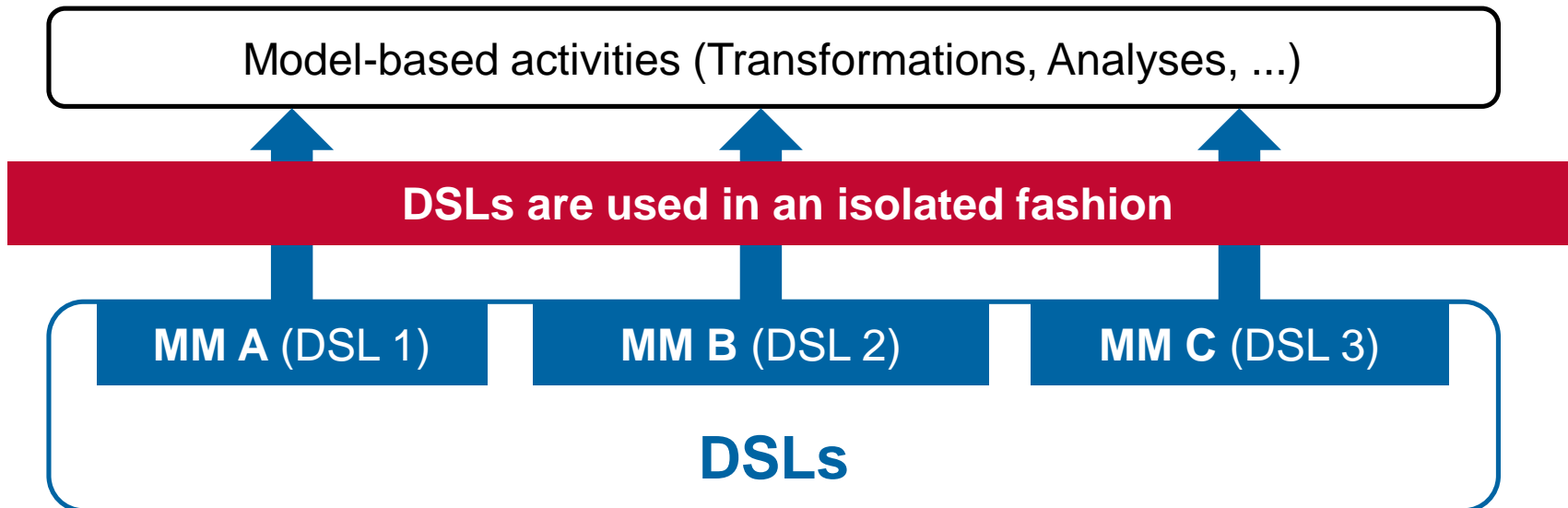
Introduction & Motivation

⇒ No common interface between MMs

- Difficult automation of FW development
- Capabilities of MD activities limited by MM

⇒ Co-design & coordination challenging

- HW/SW codesign common practice
- Prolongs FW development cycle
- Can lead to late detection of design errors



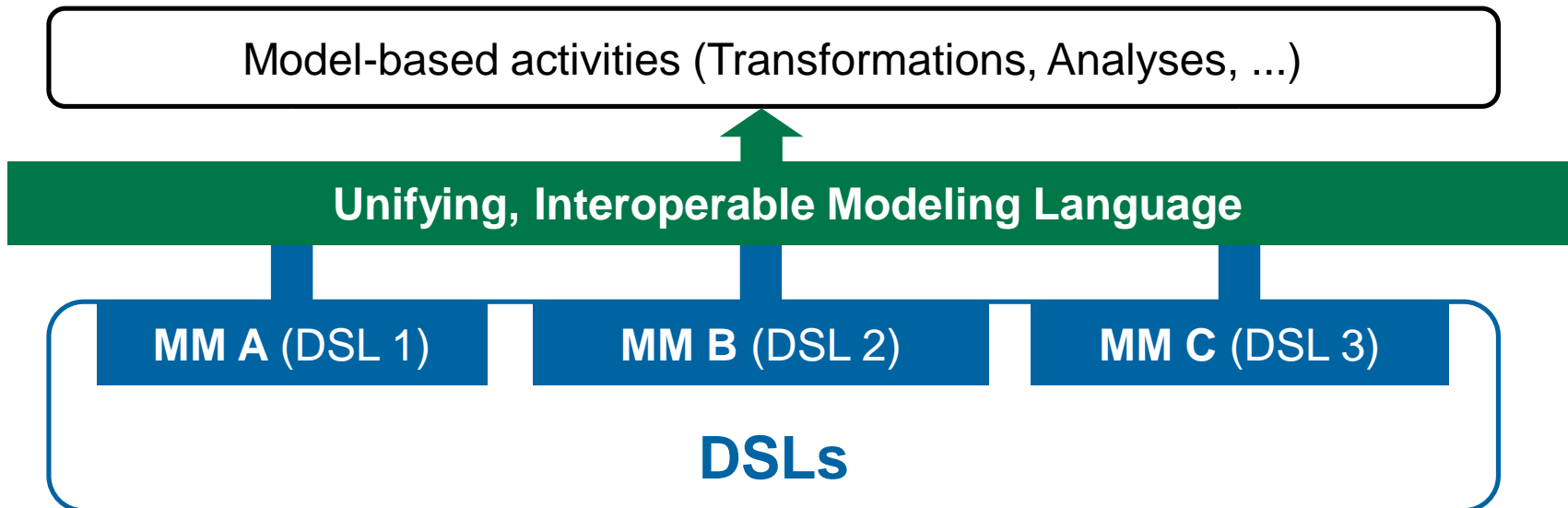
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Introduction & Motivation

- ⇒ **Exploit data synergies via common interface**
 - Expand capabilities of MD activities
- ⇒ **Easier co-design & coordination**
 - Shorter FW development cycle
 - Earlier detection of design errors
- **Holistic approach to the automated synthesis of FW**

Model-based activities (Transformations, Analyses, ...)

Unifying, Interoperable Modeling Language

MM A (DSL 1)

MM B (DSL 2)

MM C (DSL 3)

DSLs

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**IoT Platform Modeling Language
(IoT-PML)**

MM A (DSL 1)

MM B (DSL 2)

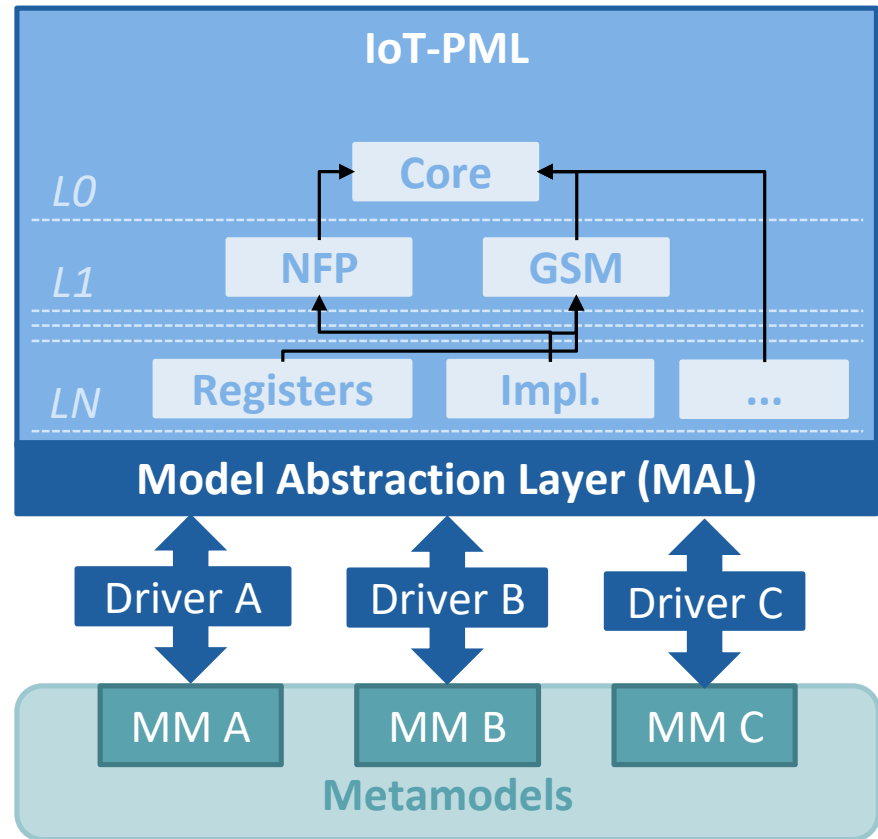
MM C (DSL 3)

DSLs

The IoT-PML

- **Basic idea:** Capture essential concepts of related MMs
 - FRs, NFRs/NFPs of SW/HW platform
 - Device configurability
 - Usage scenarios
- Common abstractions of these concepts to enable effective integration and cooperation
 - Careful analysis necessary, as we do **not** want to create a gargantuan metamodel
 - Provide data exchange at model runtime via a model abstraction layer
- Support for top-down and bottom-up workflows
- MOF-conformant metamodel
 - Currently implemented as a UML profile

The IoT-PML – Architecture & Features



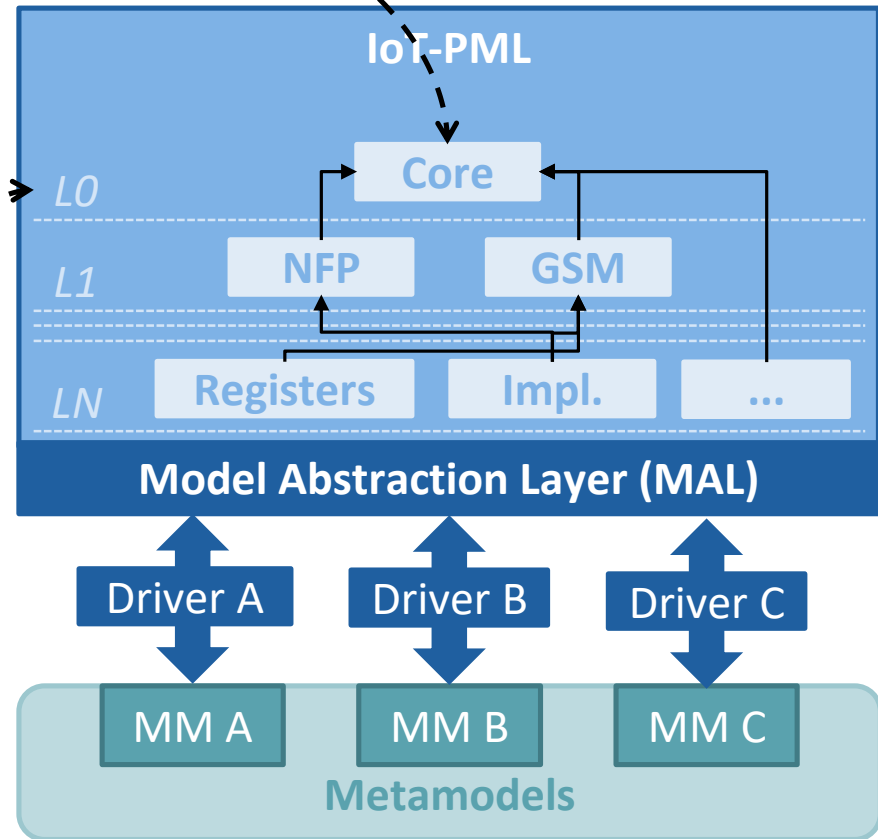
The IoT-PML – Architecture & Features

Modular

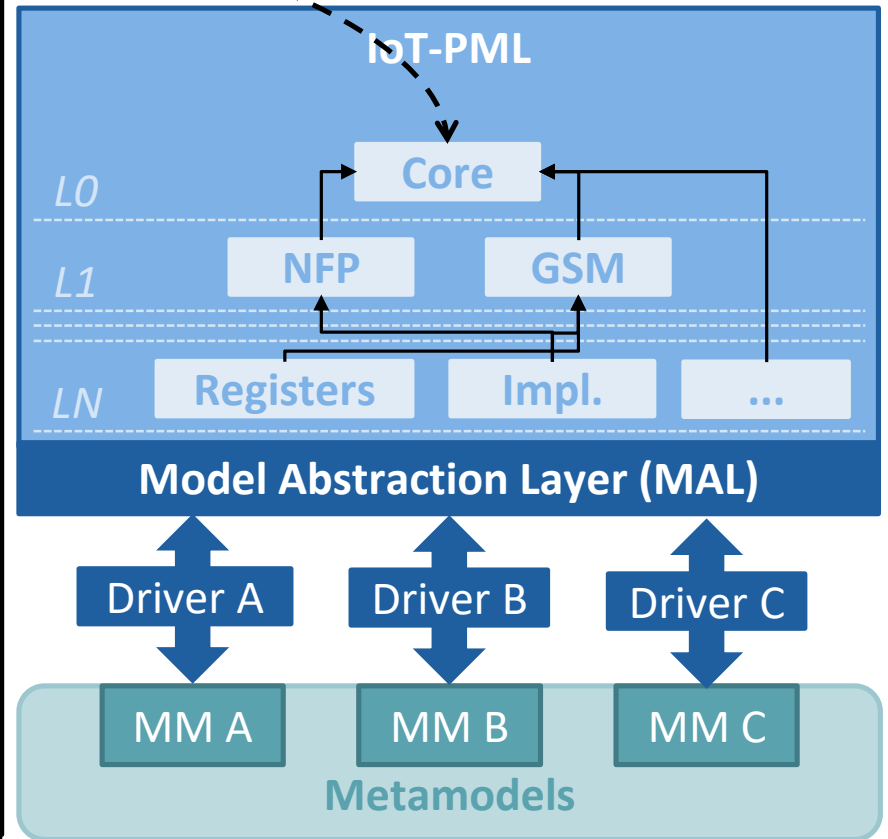
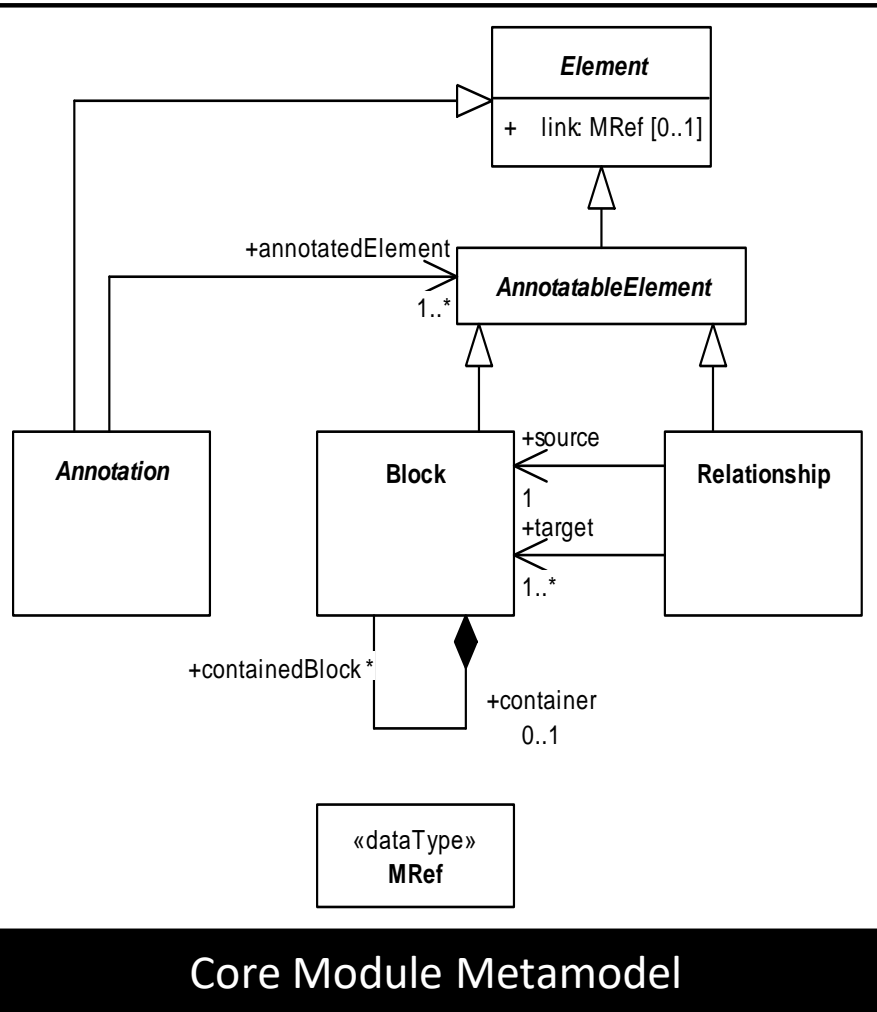
- **Module:** contains **concepts** common to a number of MMs
- **Concepts** have to specialize concepts of the Core module

Layered

- **Layer:** contains **concepts** at a particular abstraction level
- ***LO*** highest, ***LN*** lowest abstraction level



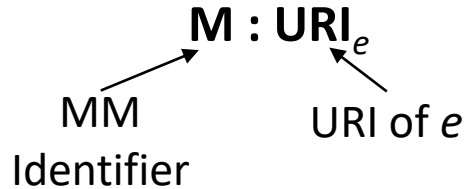
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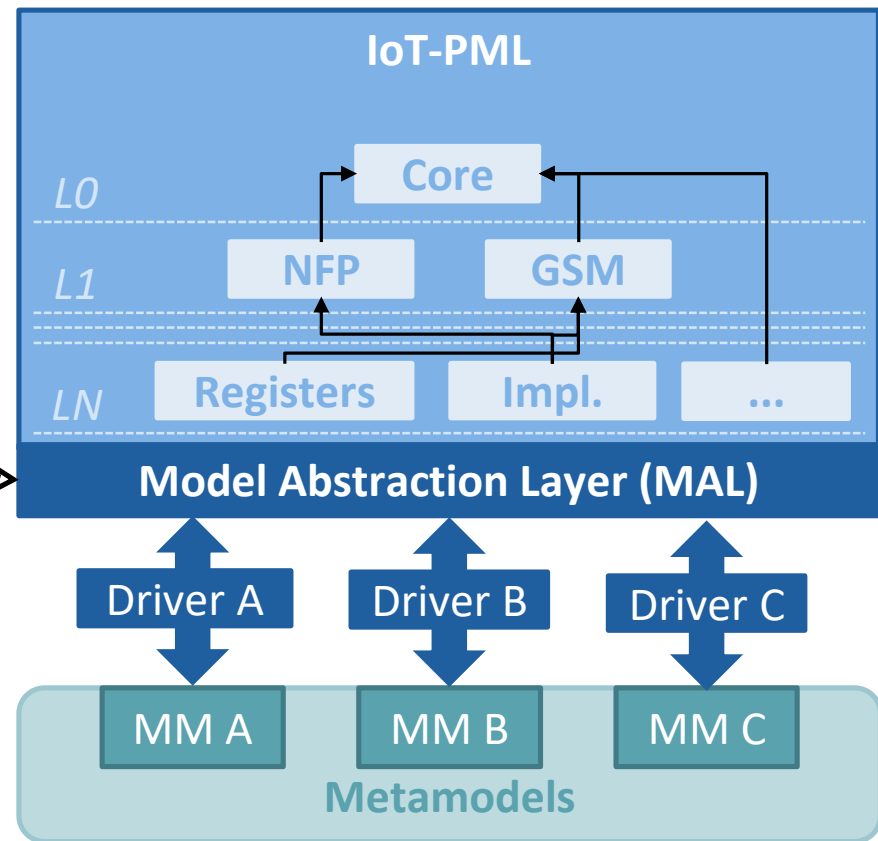
The IoT-PML – Architecture & Features

Model Linkage

- Each IoT-PML element can link to an external model element e using a **model reference**



- Linkage at model runtime facilitated by **Model Abstraction Layer (MAL)**
 - Module-specific interfaces, which are implemented by metamodel-specific drivers



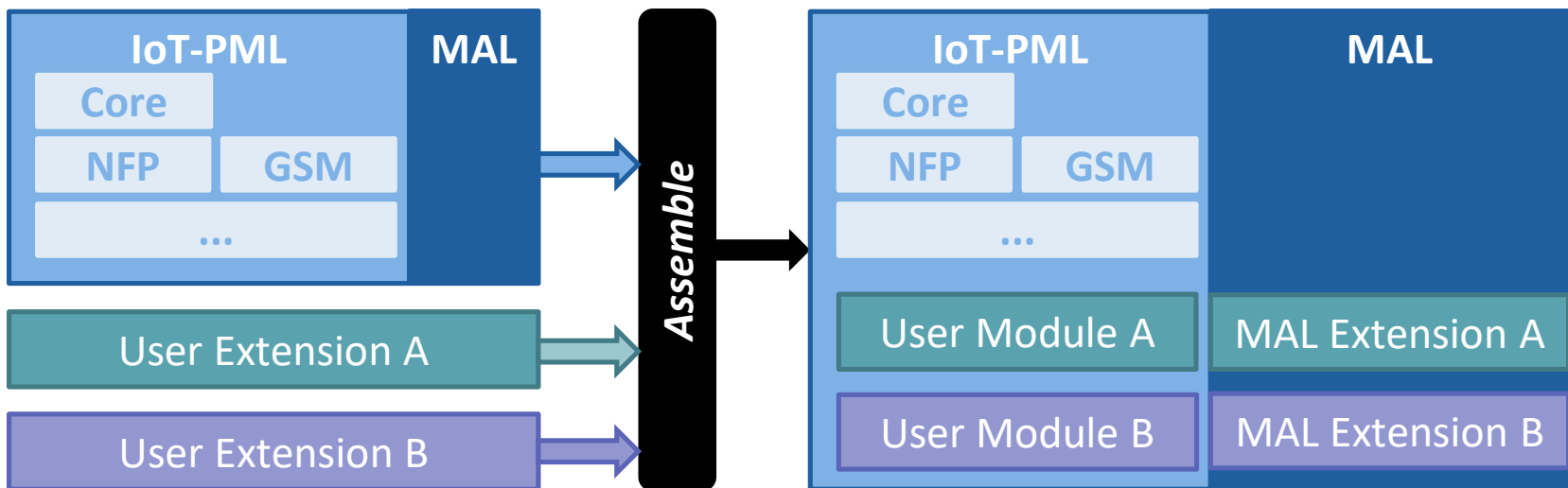
The IoT-PML – Architecture & Features

■ Extensibility

■ User modules can be added to the IoT-PML

- New concepts, refinement of existing concepts
- Extend MAL with corresponding interfaces

■ IoT-PML and MAL are constructed at model runtime by assembling built-in and user modules

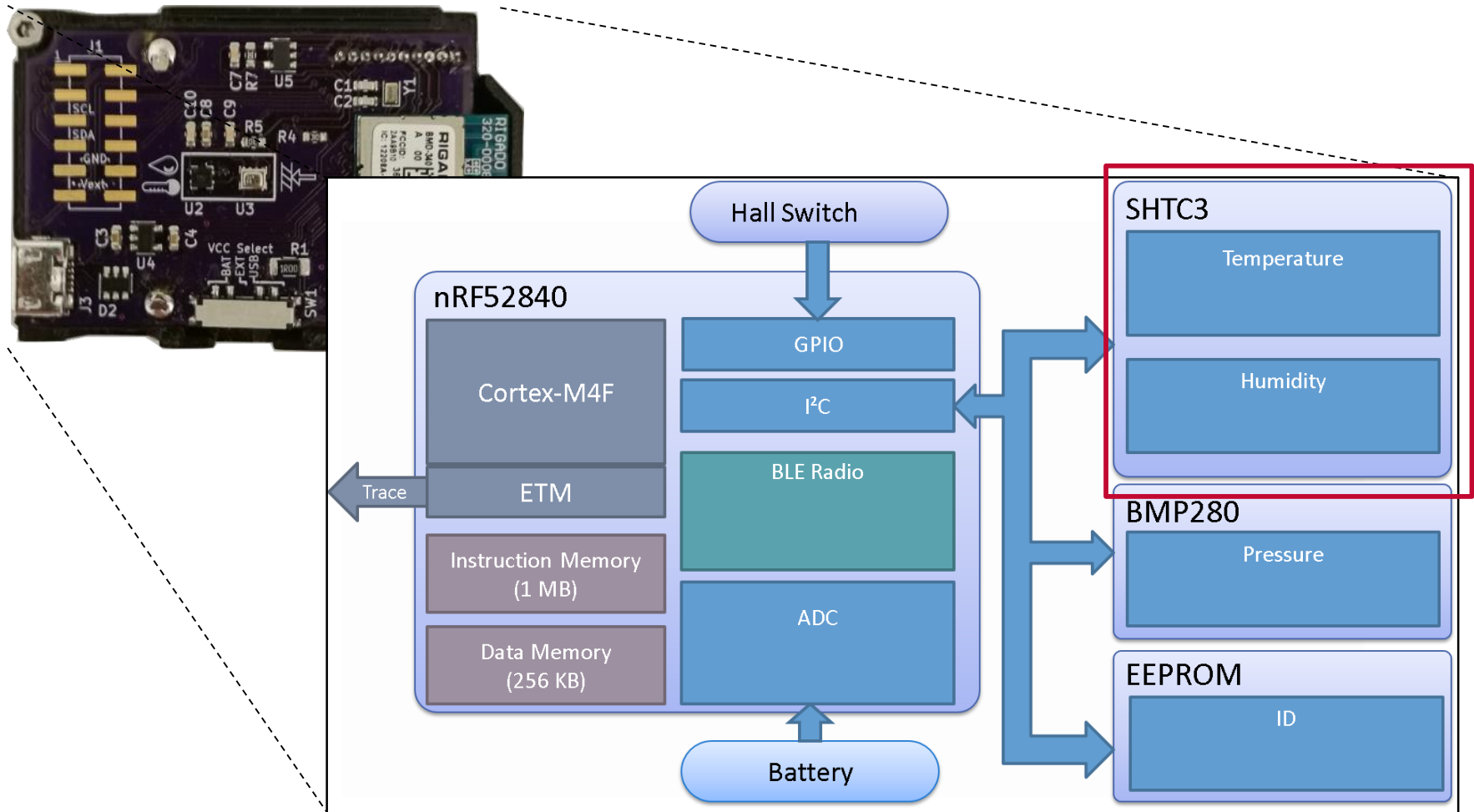


The IoT-PML – Implementation

- Currently implemented as a UML profile
 - Layer \leftrightarrow package, module \leftrightarrow (sub)profile, concept \leftrightarrow stereotype
 - Exploit UML for modeling SW aspects
 - Leverage large ecosystem of model-based technologies (M2M, M2T, ...) that evolved around OMG standards
 - Mature tooling support
- Realized using Eclipse-based frameworks and tools
 - EMF
 - Papyrus Modeling Environment

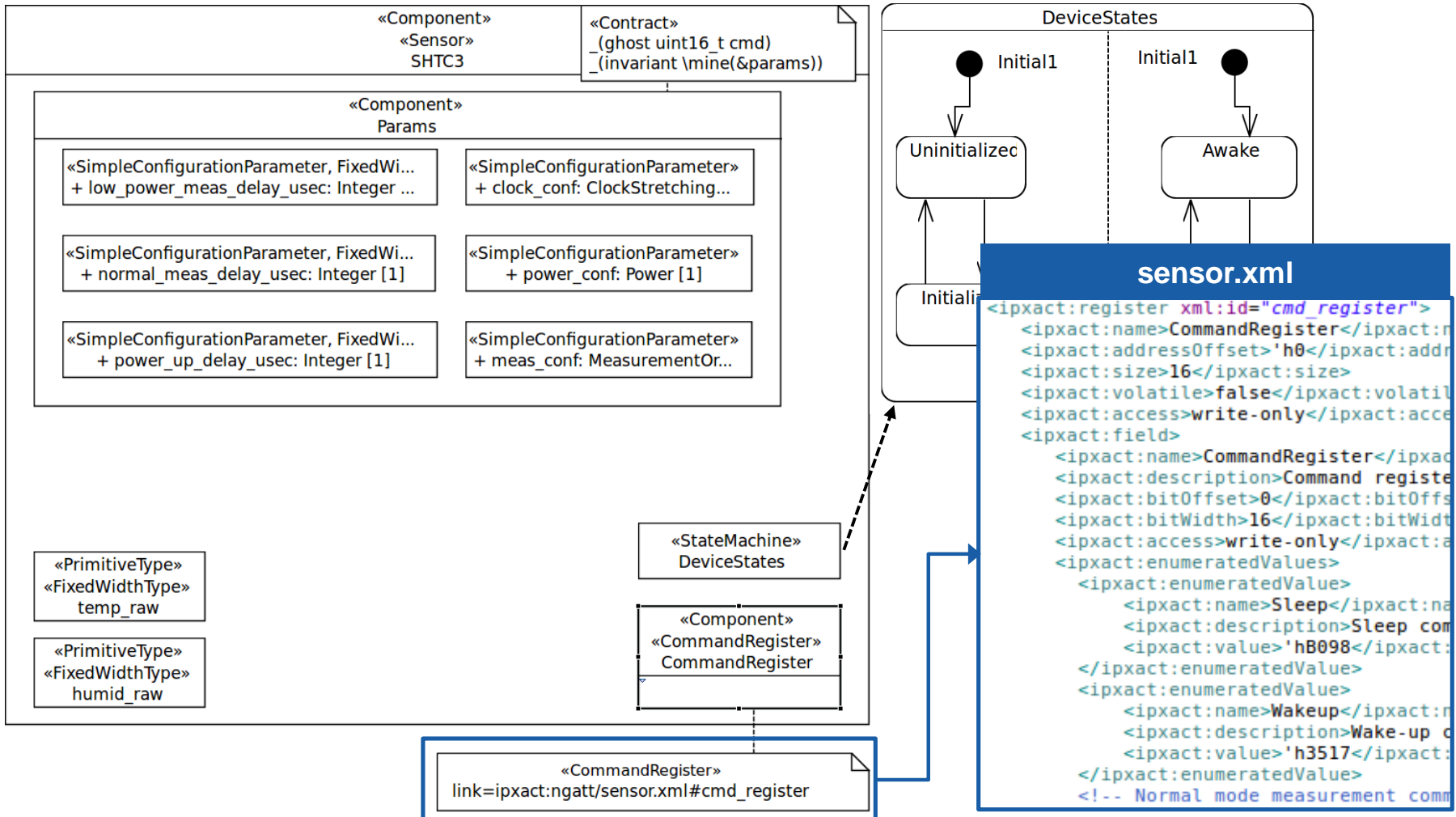
Use Case

- Code generation (and verification) of a driver for an IoT sensor device peripheral



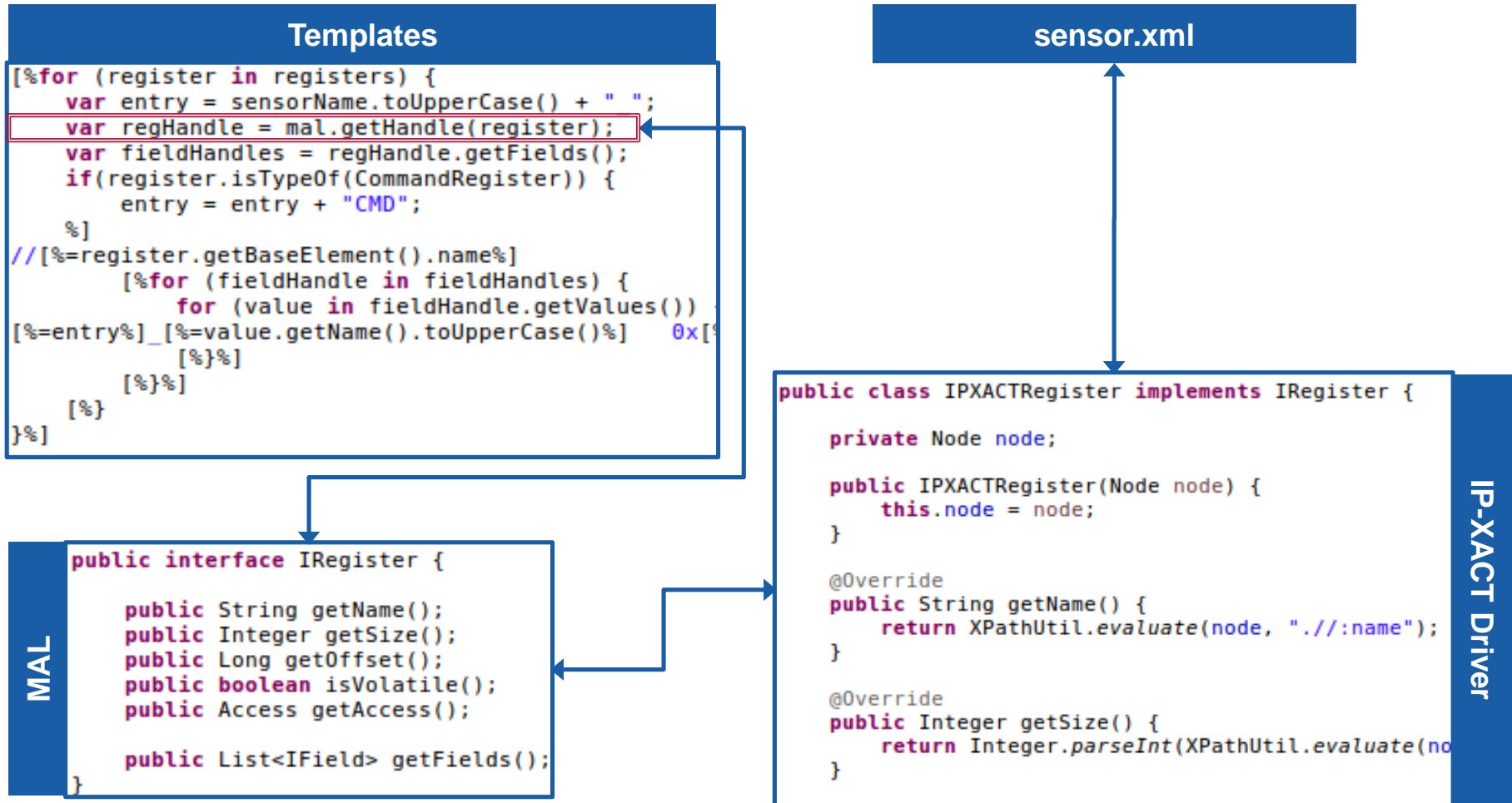
Use Case

- Basic top-down workflow
 - Generate SW-centric IoT-PML model of peripheral driver



Use Case

- Basic top-down workflow
 - Use templates to generate driver skeleton



Conclusions

- First concept of novel unifying modeling language for ultra-thin IoT device FW
 - Linking mechanism enables data exchange with external metamodels
 - MOF-conformant, currently implemented as a UML profile
- Language development and analysis of metamodels still ongoing
 - New external metamodel: device trees
- Rudimentary tool support
 - Currently working on Papyrus integration of MAL features
 - Need to keep data consistent between IoT-PML \leftrightarrow external model
- Generic mechanism to map IoT-PML concepts to arbitrary XSD-based metamodels
 - Automatic generation of MAL drivers
 - Could be extended to arbitrary metamodels (e.g. text-based)

Thank you for your attention!

Any questions?